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Ia 64 Linux Kernel Design

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IA-64 Linux Kernel: Design and Implementation | InformIT

David Mosberger, Stéphane Eranian. Published 2002. Computer Science. From the Book: PREFACE This book grew out of the simple desire to describe exactly how Linux works on an IA-64 machine. By realizing that desire, we hope not only to shine a light on the inner workings of Linux, but also to share some of the excitement and the creative processes that are involved in solving the many technical challenges that arise when designing an operating system for a platform as radical and innovative ...

[PDF] IA-64 Linux Kernel: Design and Implementation ...

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IA-64 Linux Kernel: Design and Implementation - David ...

IA-64 is a high-performance microprocessor architecture co-designed by HP and Intel. The titanium chip is the first implementation of this new architecture. Intel already has outlined a roadmap of several chips to follow, such as the more powerful McKinley and Deerfield implementations. To emphasize the longevity of this book, as well as the Linux kernel it describes, it focuses on the IA-64 architecture rather than specific implementations, such as Itanium or McKinley.

Mosberger & Eranian, IA-64 Linux Kernel: Design and ...

developer in ia 64 linux kernel design and implementation the kernel projects leaders systematically present every major subsystem introducing interfaces used by linux to abstract platform differences showing how these interfaces are used in ia 64 and illuminating key issues

Ia 64 Linux Kernel Design And Implementation PDF

IA-64 Architecture. Linux kernel release for the IA-64 Platform. Memory Attribute Aliasing on IA-64. EFI Real Time Clock driver. IPF Machine Check (MC) error inject tool. Light-weight System Calls for IA-64. IRQ affinity on IA64 platforms. An ad-hoc collection of notes on IA64 MCA and INIT processing. Serial Devices.

IA-64 Architecture — The Linux Kernel documentation

Design Goals & Approach IPure 64-bit kernel for IA-64 (no legacy) IAPIs compatible with Linux/x86 wherever possible (e.g., error-, signal-, ioctl-codes) IMinimize changes to platform-independent code (started with 2.1.126, now at 2.3.35)

IA-64 Linux Kernel Technical Update

IA-64 Architecture. Linux kernel release for the IA-64 Platform; Memory Attribute Aliasing on IA-64. ... The ones of most interest to the Linux kernel are: WB: Write-back (cacheable) UC: Uncacheable: WC: Write-coalescing: System memory typically uses the WB attribute. ... The design of the chipset determines which attributes are supported on ...

Memory Attribute Aliasing on IA-64 — The Linux Kernel ...

In early 1998, he founded the project to bring Linux to the IA-64 platform, later developing the first IA-64 version of the GNU C compiler and GNU toolchain. He has been serving as lead architect, developer, and gatekeeper of the IA-64 Linux kernel source code.

Mosberger & Eranian, IA-64 Linux Kernel: Design and ...

Intel later called it IA-64, then Itanium Processor Architecture (IPA), before settling on Intel Itanium Architecture, but it is still widely referred to as IA-64. It is a 64-bit register-rich explicitly parallel architecture. The base data word is 64 bits, byte-addressable. The logical address space is 2⁶⁴ bytes.

IA-64 - Wikipedia

1. Introduction¶. This document describes the efitrc.c driver has provided for the IA-64 platform. The purpose of this driver is to supply an API for kernel and user applications to get access to the Time Service offered by EFI version 0.92.

EFI Real Time Clock driver — The Linux Kernel documentation

IA-64 Linux Kernel: Design and Implementation . 2001. Abstract. From the Book: PREFACE This book grew out of the simple desire to describe exactly how Linux works on an IA-64 machine. By realizing that desire, we hope not only to shine a light on the inner workings of Linux, but also to share some of the excitement and the creative processes ...

IA-64 Linux Kernel | Guide books

To maximize performance, IA-64 supports multiple page sizes and Linux can be configured to use a size of 4, 8, 16, or 64 Kbytes. In the figure, the 64-bit address space is divided into 16 pages, meaning that each virtual page would have a size of 2⁶⁴ /16 = 2⁶⁰ bytes or 1024 Pbytes (1 Pbyte = 2⁵⁰ bytes).

Virtual Memory in the IA-64 Linux Kernel | Introduction to ...

This is a well written ia64/Linux kernel book which explains the internal Linux kernel working such as process, task, virtual memory, kernel entry and exit, device IO. Each chapter consist of kernel explanation about the topic, API explanation and ia64 implementation.

Amazon.com: Customer reviews: IA-64 Linux Kernel: Design ...

The IA-64 architecture leaves context and address-space switching entirely to software, so there is no problem in keeping the two operations separate. Thus, switch_mm () can be implemented simply as a call to activate_mm (), passing the first two arguments, prev_mm and next_mm, and dropping the remaining three arguments.

Switching Address Spaces | Virtual Memory in the IA-64 ...

4.6.2 IA-64 implementation The IA-64 architecture guarantees that virtual aliases are supported in hardware but leaves open the possibility that on certain CPUs there may be a performance penalty if two virtual addresses map to the same memory location and the addresses do not differ by a value that is an integer multiple of 1 Mbyte.

Memory Coherency | Virtual Memory in the IA-64 Linux ...

The Linux Kernel 5.8.0-rc7 The Linux kernel user's and administrator's guide

L2TP — The Linux Kernel documentation

The Linux Kernel 5.8.0-rc7 The Linux kernel user's and administrator's guide